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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10081992	FILING DATE 02/21/2002	CLASS 438	SUBCLASS	GAU 2812	EXAMINER POMPEY, R
**APPLICANTS: Jin Been-Yih; Arghavani Reza; Chau Robert; <i>RP yes</i>					
**CONTINUING DATA VERIFIED: <i>RP none</i>					
** FOREIGN APPLICATIONS VERIFIED: <i>RP none</i>					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's initials <i>RP</i>		<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO 10559-587001 / P12768	
TITLE : Non-silicon semiconductor and high-k gate dielectric metal oxide semiconductor field effect transistors					
U.S. DEPT. OF COMM/PAT. & TM-PTO-436L (Rev. 12-94) 14)					

NOTICE OF ALLOWANCE MAILED		Assistant Examiner		CLAIMS ALLOWED	
				Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner		DRAWING	
Amount Due	Date Paid			Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE		Application Examiner	
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